



10/6/7,706

IFW

Docket No.: M4065.0100/P100-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Patent No.: 6,930,337

Issued: August 16, 2005

For: CMOS IMAGER WITH SELECTIVELY
SILICIDED GATES

STATUS INQUIRY

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive a Reissued Certificate of Correction from the Patent and Trademark Office in response to our Request dated January 27, 2006.

Dated: August 14, 2006

Respectfully submitted,

By 

Thomas J. D'Amico
DICKSTEIN SHAPIRO LLP
Registration No.: 28,371
Elizabeth Parsons
Registration No.: 52,499
1825 Eye Street, NW
Washington, DC 20006-5403
(202) 420-2200
Attorney for Applicant